

TECHNICAL DATA
DATA SHEET 4284, REV. E

HERMETIC SILICON CARBIDE RECTIFIER

DESCRIPTION: A 600-VOLT, 8 AMP POWER SILICON CARBIDE RECTIFIER IN A HERMETIC TO-257 PACKAGE AVAILABLE SCREENED TO ANY REQUIRED LEVEL

FEATURES:

- NO RECOVERY TIME OR REVERSE RECOVERY LOSSES
- NO TEMPERATURE INFLUENCE ON SWITCHING BEHAVIOR
- **High Frequency Option** - Non-magnetic Glidcop leads are available for improved performance at high frequency; use part number prefix SHDG
- **Ceramic Seal Option** – For ceramic seals use part number prefix SHDC

MAXIMUM RATINGS

ALL RATINGS ARE @ $T_C = 25\text{ }^\circ\text{C}$ UNLESS OTHERWISE SPECIFIED.

RATING	SYMBOL	MAX.	UNITS
PEAK INVERSE VOLTAGE	PIV	600	Volts
MAXIMUM DC OUTPUT CURRENT (With $T_C = 65\text{ }^\circ\text{C}$, for part numbers with P and N suffixes)	I_O	8	Amps
MAXIMUM DC OUTPUT CURRENT (With $T_C = 65\text{ }^\circ\text{C}$, for part numbers with Single and D suffixes)	I_O	4	Amps
MAXIMUM REPETITIVE FORWARD SURGE CURRENT PER LEG ($t = 8.3\text{ms}$, Sine) per leg, $T_C = 25\text{ }^\circ\text{C}$	I_{FRM}	20	Amps
MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT PER LEG ($t = 10\mu\text{s}$, Pulse) per leg, $T_C = 25\text{ }^\circ\text{C}$	I_{FSM}	110	Amps
MAXIMUM POWER DISSIPATION, $T_C = 25\text{ }^\circ\text{C}$,	P_d	20	W
MAXIMUM THERMAL RESISTANCE, Junction to Case (PER DUAL PACKAGE For Common Cathode/Anode Configurations)	$R_{\theta JC}$	5.6	$^\circ\text{C/W}$
MAXIMUM OPERATING AND STORAGE TEMPERATURE RANGE*	Top, Tstg	-55 to +200	$^\circ\text{C}$

* Note: SiC semiconductors will handle at or above this operating and storage temperature. However, extended operational use of the packaged device above 175C may reduce its future performance. All qualification testing and screening per MIL-PRF-19500 will only be performed to 175C.

ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	TYP	MAX.	UNITS
MAXIMUM FORWARD VOLTAGE DROP Pulsed ($I_f = 4\text{ A PER LEG}$) V_f	$T_J = 25\text{ }^\circ\text{C}$ 1.50 $T_J = 175\text{ }^\circ\text{C}$ 2.00	1.85 2.40	Volts
MAXIMUM REVERSE CURRENT ($I_r @ 600\text{V PIV PER LEG}$)	$T_J = 25\text{ }^\circ\text{C}$ 0.025 $T_J = 175\text{ }^\circ\text{C}$ 0.050	0.200 1.0	mA
TOTAL CAPACITIVE CHARGE ($V_R=600\text{V } I_F=4\text{A } di/dt=500\text{A}/\mu\text{s } T_J=25\text{ }^\circ\text{C}$) Q_C per leg	10	N/A	nC
MAXIMUM JUNCTION CAPACITANCE ($V_f=5\text{V}$) per leg	C_T 220		pF

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Figure 1. Forward Characteristics

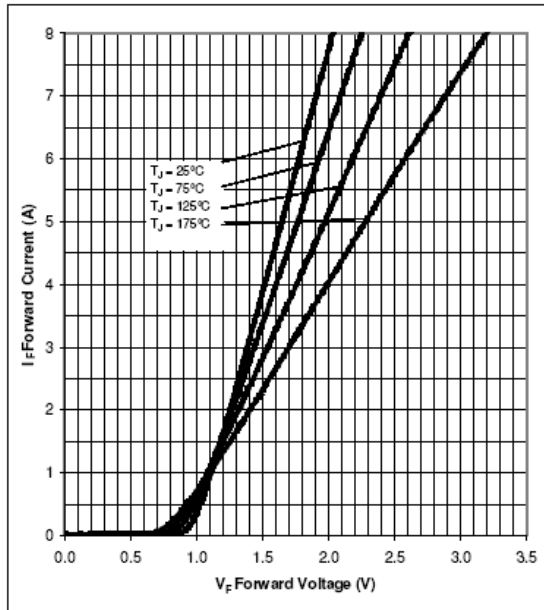


Figure 2. Reverse Characteristics

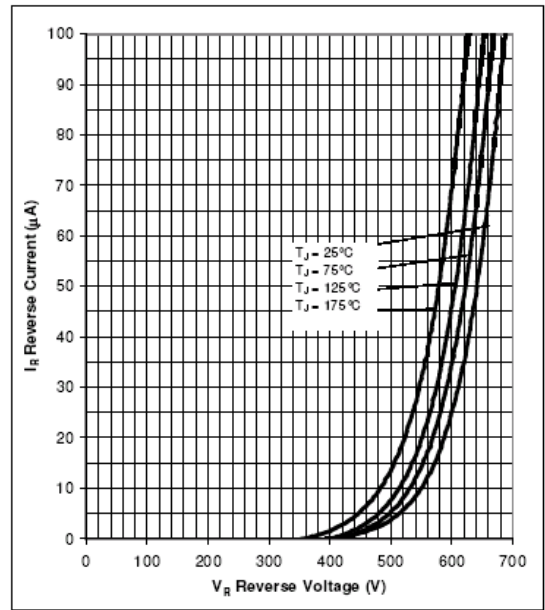


Figure 3. Current Derating

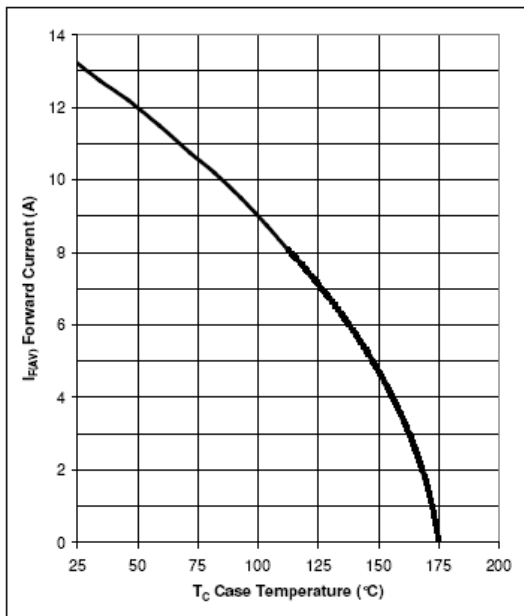
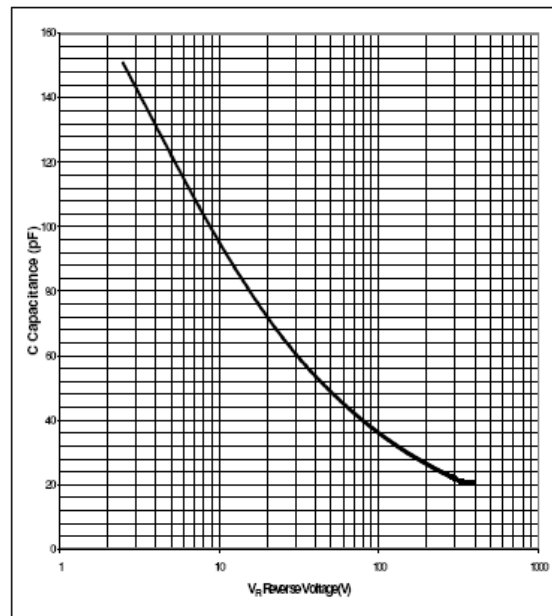


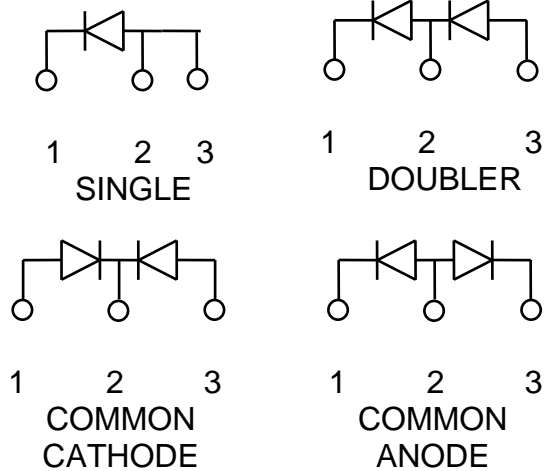
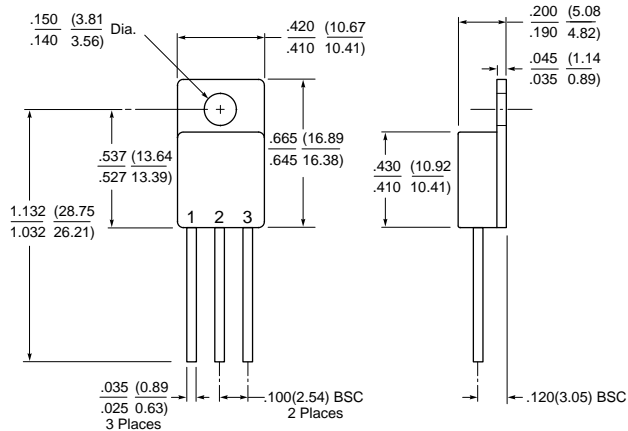
Figure 4. Capacitance vs. Reverse Voltage



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MECHANICAL DIMENSIONS

TO-257



PINOUT TABLE

TYPE	PIN 1	PIN 2	PIN 3
SINGLE RECTIFIER	CATHODE	ANODE	ANODE
DUAL RECTIFIER/COMMON CATHODE (P)	ANODE 1	COMMON CATHODE	ANODE 2
DUAL RECTIFIER/COMMON ANODE (N)	CATHODE 1	COMMON ANODE	CATHODE 2
DUAL RECTIFIER/DOUBLER (D)	ANODE	ANODE/CATHODE	CATHODE

Application Note: Customers should be aware that at the current stage of technical development of SiC, the reverse avalanche capabilities of the device are limited.

Customer designs will need to accommodate these limitations and avoid exposure of the device to this and other potentially damaging conditions in their applications.

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