

TECHNICAL DATA
DATA SHEET 1012, REV Formerly Part Number SHD2185/A/B

# HERMETIC POWER MOSFET N-CHANNEL

### **FEATURES:**

- 500 Volt, 0.3 Ohm, 9.0A MOSFET
- Low R<sub>DS (on)</sub>
- Equivalent to IRF450 Series

## **MAXIMUM RATINGS**

ALL RATINGS ARE AT  $T_A = 25^{\circ}\text{C}$  UNLESS OTHERWISE SPECIFIED.

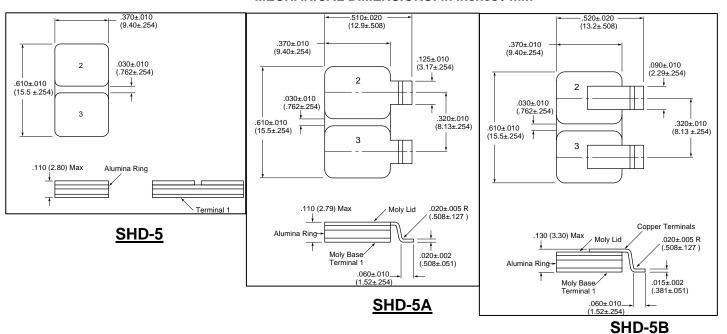
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	$V_{GS}$		-	±20	Volts
CONTINUOUS DRAIN CURRENT @ $T_C = 25^{\circ}C$	I <sub>D</sub>	-	-	12	Amps
PULSED DRAIN CURRENT @ T <sub>C</sub> = 25°C	I <sub>DM</sub>	-	-	48	Amps(pk)
OPERATING AND STORAGE TEMPERATURE	$T_{OP}/T_{STG}$	-55	-	+150	°C
TERMAL RESISTANCE JUNCTION TO CASE	$R_{\theta JC}$	-	-	0.6	°C/W
TOTAL DEVICE DISSIPATION @ T <sub>C</sub> = 25°C	$P_{D}$	-	-	200	Watts

## **ELECTRICAL CHARACTERISTICS**

CHARACTERISTIC	SYMBOL	MIN.	_ TYP	MAX.	UNITS
DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV <sub>DSS</sub>	500	-	-	Volts
$V_{GS} = 0V, I_{D} = 250 \mu A$					
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	$V_{GS(th)}$	2.0	-	4.0	
DRAIN TO SOURCE ON STATE RESISTANCE					
$V_{GS} = 10 Vdc, I_D = 8.0 A$	R <sub>DS(on)</sub>	-	-	0.415	Ω
PULSE TEST, $t \le 300 \mu s$ , DUTY CYCLE $d \le 2\%$					
ZERO GATE VOLTAGE DRAIN CURRENT		-	-		
$V_{DS} = 0.8xMax$ . Rating, $T_{J} = 25$ °C	I <sub>DSS</sub>			25	μΑ
$V_{GS} = 0 Vdc, T_J = 125^{\circ}C$				250	mA
GATE TO BODY LEAKAGE CURRENT $V_{GS} = \pm 20 \text{Vdc}$ ,	$I_{GSS}$	-	-	±100	nA
$V_{DS} = 0$					
TOTAL GATE CHARGE $V_{GS} = 10 \text{ Vdc}$	$Q_g$	55	-	120	nC
GATE TO SOURCE CHARGE $V_{DS} = 0.5VxMax$ . Rating,	$Q_gs$	5.0		19	
GATE TO DRAIN CHARGE $I_D = 12A$	$Q_{gd}$	27		70	
TURN ON DELAY TIME $V_{DD} = 250V$ ,	$t_{d(ON)}$	-	-	35	nsec
RISE TIME $I_D = 12$ ,	t <sub>r</sub>			190	
TURN OFF DELAY TIME $R_G = 2.35\Omega$	t <sub>d(OFF)</sub>			170	
FALL TIME	t <sub>f</sub>			130	
FORWARD VOLTAGE $I_S = 12A, V_{GS} = 0V$	$V_{SD}$	-	-	1.7	Volts
PULSE TEST, t ≤ 300 μs, DUTY CYCLE d ≤ 2%					
REVERSE RECOVERY TIME $I_F = 12A$	t <sub>rr</sub>	-	1600	-	nsec
REVERSE RECOVERY CHARGE di/dt = 100A/μsec					
$V_{DD} \le 50V$	$Q_{RR}$		14		μС
INPUT CAPACITANCE $V_{DS} = 25 \text{ Vdc},$	C <sub>iss</sub>	-	2700	-	pF
OUTPUT CAPACITANCE $V_{GS} = 0 \text{ Vdc},$	$C_{oss}$		600		
REVERSE TRANSFER CAPACITANCE f = 1 MHz	$C_{rss}$		240		
DRAIN TO CASE CAPACITANCE	C <sub>DC</sub>		12		

### DATA SHEET, 1012 REV. -

#### **MECHANICAL DIMENSIONS: in Inches / mm**



#### PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3
MOSFET, SURFACE MOUNT	DRAIN	SOURCE	GATE
SHD-5, 5A, 5B PACKAGE			

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