SENSITRON SEMICONDUCTOR

TECHNICAL DATA DATA SHEET 1064, REV. -

HERMETIC POWER MOSFET N-CHANNEL

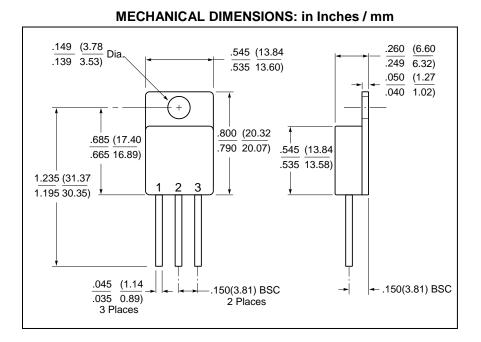
DESCRIPTION: A 500 VOLT, 0.415 OHM, 12A MOSFET IN A HERMETIC TO-254 PACKAGE.

MAXIMUM RATINGS	ALL RATINGS ARE AT $T_A = 25^{\circ}C$ UNLESS OTHERWISE SPECIFIED.					
RATING		SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE		V_{GS}	-	-	±20	Volts
CONTINUOUS DRAIN CURRENT	@ T _C = 25°C	I _D	-	-	12	Amps
PULSED DRAIN CURRENT	@ T _C = 25°C	I _{DM}	-	-	48	Amps(pk)
OPERATING AND STORAGE TEMPERATURE		T _{OP} /T _{STG}	-55	-	+150	°C
THERMAL RESISTANCE JUNCTION TO CASE		$R_{ ext{ heta}JC}$	-	-	0.83	°C/W
TOTAL DEVICE DISSIPATION @ T _C = 25°C		PD	-	-	150	Watts

ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNITS
DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV _{DSS}	500	-	-	Volts
$V_{GS} = 0V, I_D = 250 \mu A$					
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$, $I_D = 250 \mu A$	V _{GS(TH)}	2.0	-	4.0	Volts
DRAIN TO SOURCE ON STATE RESISTANCE					
$V_{GS} = 10 V dc, I_{D} = 8.0 A$	R _{DS(ON)}	-	-	0.415	Ω
PULSE TEST, t \leq 300 μ s, DUTY CYCLE d \leq 2%					
ZERO GATE VOLTAGE DRAIN CURRENT		-	-		
V _{DS} = Max. Rating, V _{GS} = 0Vdc	I _{DSS}			25	μA
V _{DS} = 0.8xMax. Rating					
$V_{GS} = 0$ Vdc, $T_{J} = 125^{\circ}$ C				250	
GATE TO BODY LEAKAGE CURRENT $V_{GS} = \pm 20 V dc$,	I _{GSS}	-	-	±100	nA
TOTAL GATE CHARGE $V_{GS} = 10 \text{ Vdc}$	Qg	55	-	120	nC
GATE TO SOURCE CHARGE $V_{DS} = 0.5V$ Max. Rating,	Q_{gs}	5.0		19	
GATE TO DRAIN CHARGE $I_D = 12A$	Q_gd	27		70	
TURN ON DELAY TIME $V_{DD} = 250V$,	t _{d(ON)}	-	-	35	nsec
RISE TIME $I_D = 12A$,	tr			190	
TURN OFF DELAY TIME $R_G = 2.35\Omega$	t _{d(OFF)}			170	
FALL TIME	t _f			130	
FORWARD VOLTAGE $I_s = 12A, V_{GS} = 0V$	V _{SD}	-	-	1.7	Volts
PULSE TEST, t \leq 300 μ s, DUTY CYCLE d \leq 2%					
REVERSE RECOVERY TIME $I_F = 12A$	t _{rr}	-	-	1600	nsec
REVERSE RECOVERY CHARGE $di/dt = 100A/\mu sec$					
$V_{DD} \le 50V$	Q _{rr}	-	-	14	μC
INPUT CAPACITANCE $V_{DS} = 25 \text{ Vdc},$	C _{iss}	-	2700	-	pF
OUTPUT CAPACITANCE $V_{GS} = 0 Vdc$,	C _{oss}		600		
REVERSE TRANSFER CAPACITANCE f = 1 MHz	C _{rss}		240		
DRAIN TO CASE CAPACITANCE	C _{DC}		12		

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<u>TO-254</u>

PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3
MOSFET, TO-254 PACKAGE	DRAIN	SOURCE	GATE

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