# HERMETIC POWER MOSFET N-CHANNEL 

## FEATURES:

- 500 Volt, 1.5 Ohm MOSFET
- Isolated and Hermetically Sealed
- Equivalent to IRFY430M

MAXIMUM RATINGS
ALL RATINGS ARE AT $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ UNLESS OTHERWISE SPECIFIED.

| RATING | SYMBOL | MIN. | TYP. | MAX. | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| GATE TO SOURCE VOLTAGE | $\mathrm{V}_{\mathrm{GS}}$ | - | - | $\pm 20$ | Volts |
| CONTINUOUS DRAIN CURRENT $\begin{array}{ll} \\ & \mathrm{V}_{\mathrm{Gs}}=10 \mathrm{~V}, \mathrm{~T}_{\mathrm{C}}=25^{\circ} \mathrm{C} \\ & \mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{~T}_{\mathrm{C}}=100^{\circ} \mathrm{C}\end{array}$ | $\mathrm{I}_{\mathrm{D}}$ | - | - | $\begin{aligned} & 4.5 \\ & 2.8 \\ & \hline \end{aligned}$ | Amps |
| PULSED DRAIN CURRENT ${ }^{\text {a }}$ T ${ }_{\text {c }}=25^{\circ} \mathrm{C}$ | $\mathrm{I}_{\mathrm{DM}}$ | - | - | 18 | Amps |
| OPERATING AND STORAGE TEMPERATURE | $\mathrm{T}_{\text {OP }} / \mathrm{T}_{\text {STG }}$ | -55 | - | +150 | ${ }^{\circ} \mathrm{C}$ |
| TERMAL RESISTANCE JUNCTION TO CASE | $\mathrm{R}_{\text {өJC }}$ | - | - | 1.67 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| TOTAL DEVICE DISSIPATION @ $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | - | - | 80 | Watts |

ELECTRICAL CHARACTERISTICS
All Characteristics are at $25^{\circ} \mathrm{C}$ unless otherwise specified.

\begin{tabular}{|c|c|c|c|c|c|}
\hline DRAIN TO SOURCE BREAKDOWN VOLTAGE
\[
\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1.0 \mathrm{~mA}
\] \& BV \({ }_{\text {DSs }}\) \& 500 \& - \& - \& Volts \\
\hline DRAIN TO SOURCE ON STATE RESISTANCE
\[
V_{G S}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=2.8 \mathrm{~A}
\] \& \(\mathrm{R}_{\mathrm{DS} \text { (ON) }}\) \& - \& - \& 1.5 \& \(\Omega\) \\
\hline GATE THRESHOLD VOLTAGE \(\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{GS}}, \mathrm{I}_{\mathrm{D}}=250 \mu \mathrm{~A}\) \& \(\mathrm{V}_{\text {GS (th) }}\) \& 2.0 \& - \& 4.0 \& Volts \\
\hline FORWARD TRANSCONDUCTANCE
\[
V_{D S} \geq 15 \mathrm{~V}, I_{D}=2.8 \mathrm{~A}
\] \& \(\mathrm{g}_{\mathrm{fs}}\) \& 1.5 \& - \& - \& \(\mathrm{S}(1 / \Omega)\) \\
\hline \(\begin{array}{cc}\text { ZERO GATE VOLTAGE DRAIN CURRENT, } \& \mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ \left(\mathrm{V}_{\mathrm{DS}}=400 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}\right), \& \mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}\end{array}\) \& Ioss \& - \& - \& \[
\begin{gathered}
\hline 25 \\
250
\end{gathered}
\] \& \(\mu \mathrm{A}\) \\
\hline \(\begin{array}{ll}\text { GATE TO SOURCE LEAKAGE FORWARD } \& V_{G S}=20 \mathrm{~V} \\ \text { GATE TO SOURCE LEAKAGE REVERSE } \& V_{G S}=-20 \mathrm{~V}\end{array}\) \& \(\mathrm{I}_{\text {gSs }}\) \& - \& - \& \[
\begin{array}{r}
\hline 100 \\
-100 \\
\hline
\end{array}
\] \& nA \\
\hline \begin{tabular}{lc}
\hline TOTAL GATE CHARGE \& \(\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}\), \\
GATE TO SOURCE CHARGE \& \(\mathrm{V}_{\mathrm{DS}}=250 \mathrm{~V}\), \\
GATE TO DRAIN CHARGE \& \(\mathrm{I}_{\mathrm{D}}=4.5 \mathrm{~A}\)
\end{tabular} \& \[
\begin{aligned}
\& \mathrm{Q}_{\mathrm{g}} \\
\& \mathrm{Q}_{g s} \\
\& \mathrm{Q}_{g d}
\end{aligned}
\] \& - \& - \& \[
\begin{gathered}
\hline 29.5 \\
4.6 \\
19.7 \\
\hline
\end{gathered}
\] \& nC \\
\hline \begin{tabular}{lr} 
TURN ON DELAY TIME \& \(\mathrm{V}_{\mathrm{DD}}=250 \mathrm{~V}\), \\
RISE TIME \& \(\mathrm{I}_{\mathrm{D}}=4.5 \mathrm{~A}\), \\
TURN OFF DELAY TIME \& \(\mathrm{R}_{\mathrm{G}}=7.5 \Omega\), \\
FALL TIME \& \(\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}\)
\end{tabular} \& \[
\begin{gathered}
\mathrm{t}_{\mathrm{d}(\mathrm{OO})} \\
\mathrm{t}_{\mathrm{r}} \\
\mathrm{t}_{\mathrm{d}(\mathrm{OFF})} \\
\mathrm{t}_{\mathrm{f}} \\
\hline
\end{gathered}
\] \& - \& - \& \[
\begin{aligned}
\& 35 \\
\& 30 \\
\& 55 \\
\& 30
\end{aligned}
\] \& nsec \\
\hline \(\begin{array}{lr}\text { DIODE FORWARD VOLTAGE } \\ \& \mathrm{T}_{J}=25^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{S}}=4.5 \mathrm{~A}, \\ \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}\end{array}\) \& \(\mathrm{V}_{\text {SD }}\) \& - \& - \& 1.4 \& Volts \\
\hline \begin{tabular}{lr} 
REVERSE RECOVERY TIME \& \(\mathrm{T}_{J}=25^{\circ} \mathrm{C}\), \\
\& \(\mathrm{I}_{\mathrm{s}}=4.5 \mathrm{~A}\), \\
\& \(\mathrm{di} / \mathrm{dt} \leq=100 \mathrm{~A} / \mu \mathrm{sec}\), \\
REVERSE RECOVERY CHARGE \& \(\mathrm{V}_{\mathrm{DD}} \leq 50 \mathrm{~V}\)
\end{tabular} \& \[
\begin{gathered}
\hline \mathrm{t}_{\mathrm{rr}} \\
\mathrm{Q}_{\mathrm{rr}}
\end{gathered}
\] \& - \& - \& 900
7.0 \& nsec

$\mu \mathrm{C}$ <br>

\hline | INPUT CAPACITANCE | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=25 \mathrm{~V}$ |
| :--- | ---: |
| OUTPUT CAPACITANCE |  |
| REVERSE TRANSFER CAPACITANCE |  | \& \[

$$
\begin{aligned}
& \mathrm{C}_{\mathrm{iss}} \\
& \mathrm{C}_{\text {oss }} \\
& \mathrm{C}_{\mathrm{rss}}
\end{aligned}
$$

\] \& - \& \[

$$
\begin{aligned}
& 650 \\
& 135 \\
& 65
\end{aligned}
$$
\] \& - \& pF <br>

\hline
\end{tabular}

# MECHANICAL DIMENSIONS: in Inches / mm 

T0-257


Lead Form Option B
PINOUT TABLE

| DEVICE TYPE | PIN 1 | PIN 2 | PIN 3 |
| :---: | :---: | :---: | :---: |
| MOSFET | DRAIN | SOURCE | GATE |
| TO-257 PACKAGE |  |  |  |

[^0]
[^0]:    DISCLAIMER:
    1- The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact the Sensitron Semiconductor sales department for the latest version of the datasheet(s).
    2-In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, medical equipment, and safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement .
    3- In no event shall Sensitron Semiconductor be liable for any damages that may result from an accident or any other cause during operation of the user's units according to the datasheet(s). Sensitron Semiconductor assumes no responsibility for any intellectual property claims or any other problems that may result from applications of information, products or circuits described in the datasheets.
    4- In no event shall Sensitron Semiconductor be liable for any failure in a semiconductor device or any secondary damage resulting from use at a value exceeding the absolute maximum rating.
    5- No license is granted by the datasheet(s) under any patents or other rights of any third party or Sensitron Semiconductor.
    6- The datasheet(s) may not be reproduced or duplicated, in any form, in whole or part, without the expressed written permission of Sensitron Semiconductor.
    7- The products (technologies) described in the datasheet(s) are not to be provided to any party whose purpose in their application will hinder maintenance of international peace and safety nor are they to be applied to that purpose by their direct purchasers or any third party. When exporting these products (technologies), the necessary procedures are to be taken in accordance with related laws and regulations.

