SENSITRON SEMICONDUCTOR

TECHNICAL DATA DATA SHEET 4077, REV. H

HERMETIC SILICON CARBIDE RECTIFIER

DESCRIPTION: A 600-VOLT, 20 AMP POWER SILICON CARBIDE RECTIFIER IN A CERAMIC HERMETIC SMD-0.5 PACKAGE

FEATURES:

- NO RECOVERY TIME OR REVERSE RECOVERY LOSSES
- NO TEMPERATURE INFLUENCE ON SWITCHING BEHAVIOR
- AVAILABLE SCREENED TO ANY REQUIRED LEVEL
- ADD SUFFIX "L" FOR RIBBON TERMINALS, (ie.SHD620051PL)

MAXIMUM RATINGS

ALL RATINGS ARE @ $T_C = 25$ °C UNLESS OTHERWISE SPECIFIED.

RATING	SYMBOL	MAX.	UNITS	
PEAK INVERSE VOLTAGE	PIV	600	Volts	
MAXIMUM DC OUTPUT CURRENT (With $T_c = 65 ^{\circ}C$ for P suffix)	Ι _Ο	20	Amps	
MAXIMUM DC OUTPUT CURRENT (With $T_c = 65 ^{\circ}C$ for Single)	lo	10	Amps	
MAXIMUM REPETITIVE FORWARD SURGE CURRENT PER LEG (t = 8.3ms, Sine) per leg, $T_{\rm C}$ = 25 $^{\rm O}{\rm C}$	I _{FRM}	50	Amps	
MAXIMUM POWER DISSIPATION, $T_c = 25 \ ^{\circ}C$ (SMD-0.5 package)	Pd	34	W	
MAXIMUM THERMAL RESISTANCE, Junction to Case PER LEG (SMD-0.5 package)	R _{θJC}	4.4	°C/W	
MAXIMUM THERMAL RESISTANCE, Junction to Case PER LEG (SMD-0.5L package, measured to the end of the leads)	R _{θJC}	36	°C/W	
MAXIMUM OPERATING AND STORAGE TEMPERATURE RANGE*	Top, Tstg	-55 to 175	°C	
* Note: SiC semiconductors will bandle at or above this operating and storage temperature. However, extended operational use of the				

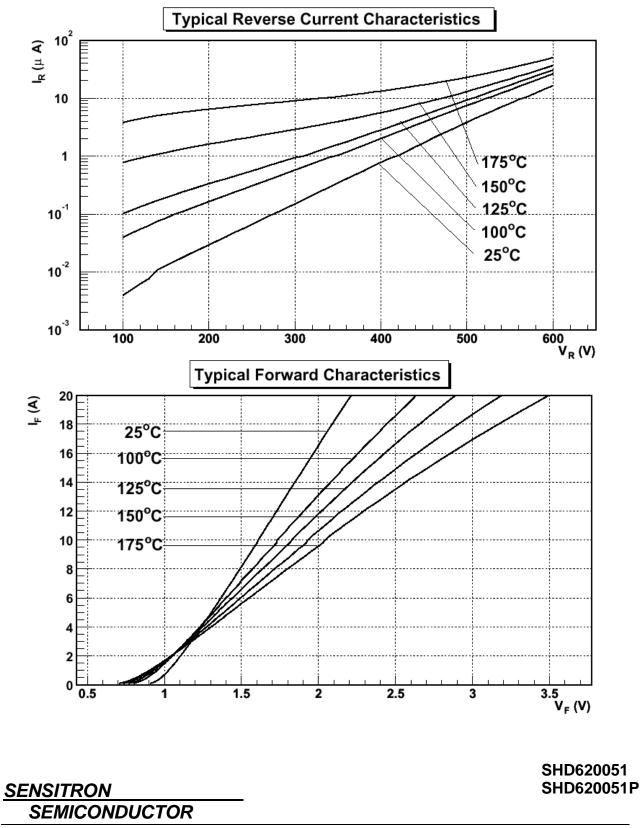
* Note: SiC semiconductors will handle at or above this operating and storage temperature. However, extended operational use of the packaged device above 175C may reduce its future performance. All qualification testing and screening per MIL-PRF-19500 will only be performed to 175C.

ELECTRICAL CHARACTERISTICS

CHARACTERISTIC		TYP	MAX.	UNITS
MAXIMUM FORWARD VOLTAGE DROP	$T_J = 25^{\circ}C$	1.65	1.80	
Pulsed (I _f = 10 A PER LEG) V_f	T _J = 150 °C	2.05	2.20	Volts
MAXIMUM FORWARD VOLTAGE DROP	$T_J = 25^{\circ}C$	1.35	1.45	
Pulsed ($I_f = 6 \text{ A PER LEG}$) V_f	T _J = 150 °C	1.60	1.70	Volts
MAXIMUM REVERSE CURRENT (Ir @ 600V PIV PER LEG)	T _J = 25 °C	0.04	0.15	
	T _J = 150 °C	0.08	0.50	mA
JUNCTION CAPACITANCE C_{T} (V _r =5V) per leg	C _T	250	350	pF
TOTAL CAPACITIVE CHARGE	Q _c per leg	35	N/A	nC
(V_R=600V I_F=20A di/dt=500A/ μ s T_J=25°C) This is design inform	mation only			

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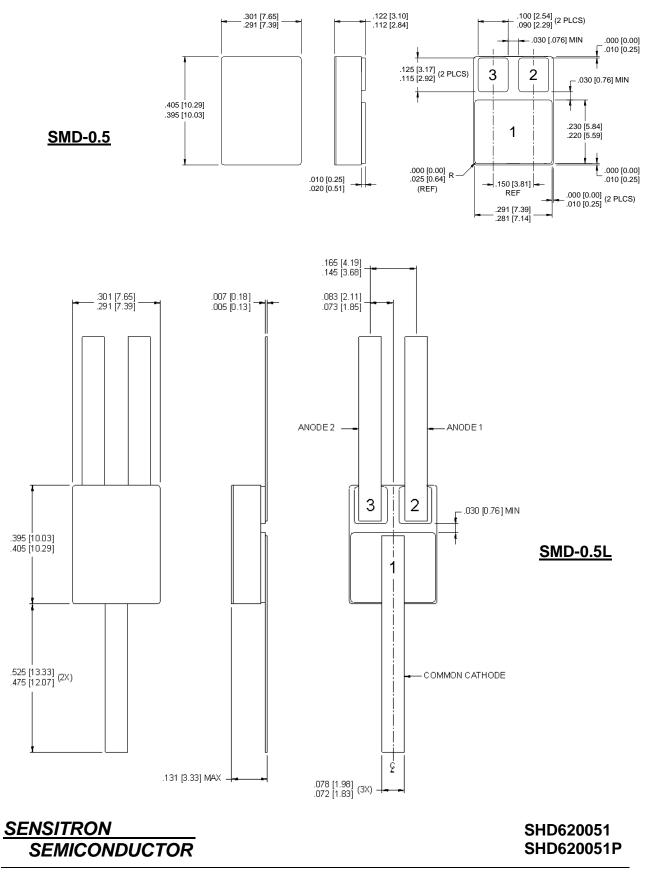
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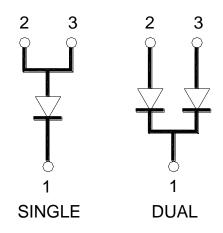
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MECHANICAL DIMENSIONS: inches / mm



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PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3
SINGLE RECTIFIER	CATHODE	ANODE	ANODE
DUAL RECTIFIER, COMMON CATHODE (P) (PL)	COMMON CATHODE	ANODE 1	ANODE 2

Application Note: Customers should be aware that at the current stage of technical development of SiC, the reverse avalanche capabilities of the device are limited.

Customer designs will need to accommodate these limitations and avoid exposure of the device to this and other potentially damaging conditions in their applications.

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