TECHNICAL DATA DATA SHEET 5362, REV.-

HERMETIC POWER MOSFET N-CHANNEL

FEATURES:

- 800 Volt, 0.8 Ohm, 12A MOSFET in TO-254 package
- Low R_{DS (on)} and Low Qg
- High Avalanche Capability
- Add "C" to the part number after SHD for Ceramic Seals

MAXIMUM RATINGS

ALL RATINGS ARE AT $T_{\rm C}$ = 25°C UNLESS OTHERWISE SPECIFIED.

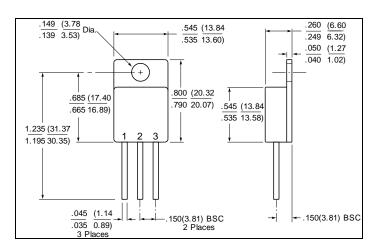
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	V_{GS}	-	-	±20	V
ON-STATE DRAIN CURRENT @ $T_C = 25^{\circ}C$	I _D	-	-	12	Α
ON-STATE DRAIN CURRENT @ $T_C = 100$ °C	I _D	-	-	7	Α
OPERATING AND STORAGE TEMPERATURE	T_{OP}/T_{STG}	-55	-	+150	°C
TOTAL DEVICE DISSIPATION @ T _C = 25°C	P_{D}	-	-	190	W
THERMAL RESISTANCE, JUNCTION TO CASE	R_{thJC}	-	-	0.66	°C/W

ELECTRICAL CHARACTERISTICS (Tc= 25°C, unless otherwise specified)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNITS
DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV _{DSS}	800	-	-	V
$V_{GS} = 0V, I_{D} = 0.25 \text{mA}$					
STATIC DRAIN TO SOURCE ON STATE RESISTANCE		-	0.62	0.80	Ω
$V_{GS} = 10V, I_{D} = 6A$	R _{DS(ON)}				
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$, $I_{DS} = 0.25$ mA	$V_{GS(th)}$	3.0	-	5.0	V
FORWARD TRANSCONDUCTANCE	g fs	-	13	-	S(1/Ω)
$V_{DS} = 50V, I_{DS} = 6A$					` ,
ZERO GATE VOLTAGE DRAIN CURRENT		-	-		
$V_{DS} = 800V, V_{GS} = 0V$	I _{DSS}			25	μΑ
$V_{DS} = 640V, V_{GS} = 0V, T_{J} = 125^{\circ}C$				250	
GATE TO SOURCE LEAKAGE FORWARD $V_{GS} = 20V$	I_{GSS}	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE V _{GS} = -20V				-100	
TURN ON DELAY TIME $V_{DD} = 400V$,	$t_{d(ON)}$	-	60	-	
RISE TIME $I_D = 12A$,	t _r		150		ns
TURN OFF DELAY TIME $R_G = 22\Omega$	$t_{d(OFF)}$		155		
FALL TIME	t _f		110		
TOTAL GATE CHARGE $I_D = 12A$,	Q_{g}	-	68	90	nC
GATE TO SOURCE CHARGE $V_{GS} = 10V$,	Q_gs	-	15	-	nC
GATE TO DRAIN CHARGE $V_{DS} = 400V$	Q_{gd}	-	32	-	nC
DIODE FORWARD VOLTAGE $T_C = 25^{\circ}C$, $I_S = 12A$,	V_{SD}	-	-	1.5	V
$V_{GS} = 0V$					
REVERSE RECOVERY CHARGE $T_J = 25^{\circ}C$,	Q_{RR}	-	12	-	μС
$di/dt = 100A/\mu s$, $I_F = 12A$					
REVERSE RECOVERY TIME $T_1 = 25^{\circ}C$,	t _{rr}	-	850		
$di/dt \le 100A/\mu s$, $I_F = 12A$					ns
INPUT CAPACITANCE $V_{GS} = 0 \text{ V}$	C _{iss}	-	2700	-	
OUTPUT CAPACITANCE $V_{DS} = 25 \text{ V}$	C_{oss}		275		pF
REVERSE TRANSFER CAPACITANCE f = 1MHz	C_{rss}		30		

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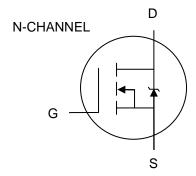
MECHANICAL DIMENSIONS: in Inches / mm



TO-254

PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3
N-CHANNEL MOSFET, IN A	DRAIN	SOURCE	GATE
TO-254 PACKAGE			



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