TECHNICAL DATA DATA SHEET 5437, REV. -

# HERMETIC SILICON CARBIDE RECTIFIER

**DESCRIPTION:** A 600-VOLT, 24 AMP COMMON CATHODE POWER SILICON CARBIDE RECTIFIER IN A CERAMIC HERMETIC LCC-5 PACKAGE.

#### **EQUIVALENT TO SSDI SSR24C60CT**

## **FEATURES:**

- NO RECOVERY TIME OR REVERSE RECOVERY LOSSES
- NO TEMPERATURE INFLUENCE ON SWITCHING BEHAVIOR
- AVAILABLE SCREENED TO S LEVEL => SHD620051PYS

## **MAXIMUM RATINGS**

ALL RATINGS ARE @ T<sub>C</sub> = 25 °C UNLESS OTHERWISE SPECIFIED.

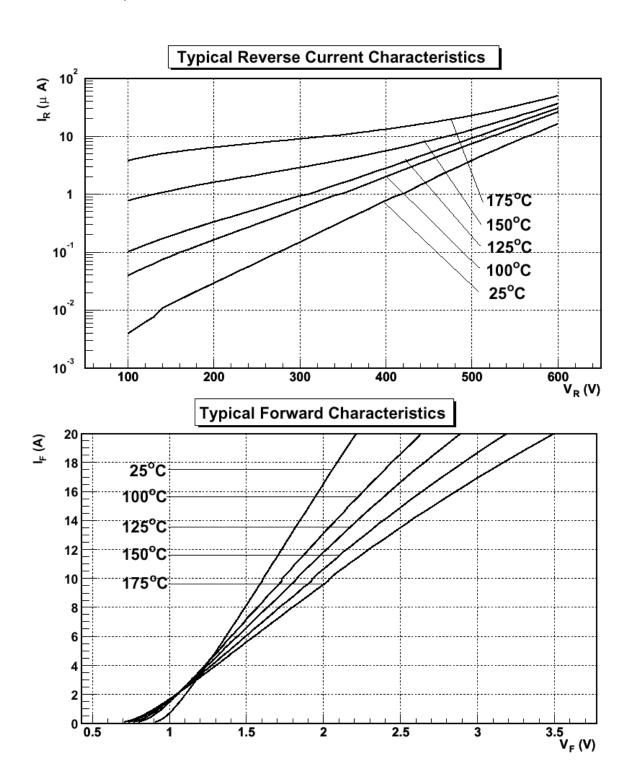
| RATING  | SYMBOL           | MAX.       | UNITS |
|---|------------------|------------|-------|
| PEAK INVERSE VOLTAGE  | PIV              | 600        | Volts |
| MAXIMUM DC OUTPUT CURRENT FOR BOTH LEGS TOGETHER  | Io               | 24         | Amps  |
| MAXIMUM DC OUTPUT CURRENT EACH LEG  | lo               | 12         | Amps  |
| MAXIMUM REPETITIVE FORWARD SURGE CURRENT PER LEG (t = 8.3ms, Sine) per leg, $T_{\rm C}$ = 25 $^{\rm O}$ C | I <sub>FRM</sub> | 50         | Amps  |
| MAXIMUM POWER DISSIPATION, T <sub>C</sub> = 25 °C   | P <sub>d</sub>   | 79         | W     |
| MAXIMUM THERMAL RESISTANCE, BOTH LEGS TOGETHER  | $R_{\theta JC}$  | 1.9        | °C/W  |
| MAXIMUM OPERATING AND STORAGE TEMPERATURE RANGE*  | Top, Tstg        | -55 to 200 | °C    |

<sup>\*</sup> Note: SiC semiconductors will handle at or above this operating and storage temperature. However, extended operational use of the packaged device above 175C may reduce its future performance. All qualification testing and screening per MIL-PRF-19500 will only be performed to 175C.

# **ELECTRICAL CHARACTERISTICS**

| CHARACTERISTIC  |                                   | TYP          | MAX.         | UNITS |
|---|-----------------------------------|--------------|--------------|-------|
| MAXIMUM FORWARD VOLTAGE DROP, Pulsed, V <sub>F</sub> , T <sub>J</sub> : | = 25°C                            | 1.20<br>1.50 | 1.38<br>1.70 | Volts |
| MAXIMUM FORWARD VOLTAGE DROP, Pulsed, $V_F$ , $T_J$ :                   | = 150°C $I_F = 6A$<br>$I_F = 12A$ | 1.35<br>1.85 | 1.55<br>2.15 | Volts |
| MAXIMUM FORWARD VOLTAGE DROP, Pulsed, $V_F$ , $T_J$ :                   | = - 55°C                          | 1.27<br>1.50 | 1.40<br>1.70 | Volts |
| MAXIMUM REVERSE CURRENT (Ir @ 600V PIV PER L                            | .EG) T <sub>J</sub> = 25 °C       | 1            | 200          |       |
|   | T <sub>J</sub> = 150 °C           | 5            | 1000         | μΑ    |
| JUNCTION CAPACITANCE C <sub>T)</sub> (V <sub>r</sub> =5V) per leg       | Ст                                | 280          | 350          | pF    |
| TOTAL CAPACITIVE CHARGE   | Q <sub>C</sub> per leg            | 35           | N/A          | nC    |
| $(V_R=10V I_F=12A di/dt=200A/\mu s T_J=25^{\circ}C)$ This is design in  | formation only                    |              |              |       |

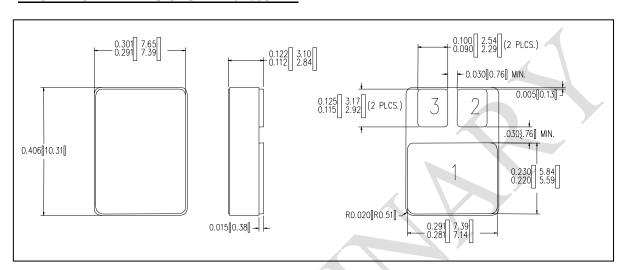
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# SENSITRON SEMICONDUCTOR

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# **MECHANICAL DIMENSIONS: IN Inches / mm**



LCC-5

## **PINOUT TABLE**

| DEVICE TYPE                        | PIN 1          | PIN 2   | PIN 3   |
|------------------------------------|----------------|---------|---------|
| DEVISE III E                       |                | 1 114 2 | 1 114 3 |
| DUAL RECTIFIER, COMMON CATHODE (P) | COMMON CATHODE | ANODE 1 | ANODE 2 |

Application Note: Customers should be aware that at the current stage of technical development of SiC, the reverse avalanche capabilities of the device are limited.

Customer designs will need to accommodate these limitations and avoid exposure of the device to this and other potentially damaging conditions in their applications.

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