SENSITRON SEMICONDUCTOR

DATASHEET 6066, REV -

900 VOLT, 90 AMP MOSFET SIX PACK MODULE

Features

- Electrically isolated, base-less construction
- Light weight, low profile standard package
- Aluminum nitride substrate
- High temperature engineering plastic shell construction



ELECTRICAL CHARACTERISTICS PER MOSFET LEG (T_J=25°C UNLESS OTHERWISE SPECIFIED)

SYMBOL	PARAMETER	MIN	ТҮР	MAX	UNIT
MOSFET S	SPECIFICATIONS				
BV _{DSS}	Drain to Source Breakdown Voltage $I_D = 100 \ \mu$ A, V _{GS} = 0V	900	-	-	V
ID	$ \begin{array}{c} \mbox{Continuous Drain Current} & T_C = \\ V_{GS} = 15V & T_C = \end{array} $	25°C - 100°C -	-	90 60	А
D(pulse)	Pulsed Drain Current, 1ms	-	-	180	А
V _{GSmax}	Gate to Source Voltage (Dynamic)	-	-	-4/+19	V
VGSop	Gate to Source Voltage (Static)	-	-	-4/+15	V
Igss	Gate-Source Leakage Current, $V_{GS} = +15V$, $V_{DS} = 0V$		10	250	nA
$V_{\text{GS(th)}}$	$ \begin{array}{ll} \mbox{Gate Threshold Voltage}, & T_J=2\\ I_D=33mA, V_{DS}=V_{GS} & T_J= \end{array} $	25°C 1.7 150°C	2.4 1.8	3.5	V
I _{DSS}	Zero Gate Voltage Drain Current $V_{DS} = 900 \text{ V}, V_{GS}=0 \text{ V}$	-	1	100	μA
RDS(on)	Drain-Source On-State Resistance $T_J = 2$ $I_D = 75A$, $V_{GS} = 15V$ $T_J =$	25°C - 150°C -	13 21	15 -	mΩ
Ciss Coss Crss	Input Capacitance Output Capacitance Reverse Transfer Cap. $V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}, \text{ V}_{AC} = 25 \text{ mV}$	-	4500 350 12		pF
t _{D(on)} t _R t _{D(off)} t _F	Turn On Delay Time Rise Time Turn Off Delay Time Fall Time $V_{DS} = 600 \text{ V}, \text{ Id} = 100\text{ A}, \text{ V}_{GS} = -4/+15\text{ V}, \text{ R}_{G} = 2.5\Omega, \text{ Inductive}$	- - - - -	48 17 60 14	- - - -	ns
Eon E _{OFF}	Turn-On Switching Energy (Body Diode FWD) Turn Off Switching Energy (Body Diode FWD) $V_{DS} = 600 \text{ V}, \text{ I}_D = 100 \text{ A}, \text{ V}_{GS} = -4/+15 \text{ V}, \text{ R}_G = 2.5 \Omega, \text{ L} = 56 \text{ u}\text{H}$		1400 830	-	μJ
R _G (int)	Internal Gate Resistance $f = 1MHz, V_{AC} = 25mV$	-	1.6	-	Ω
Q _{GS} Q _{GD} Q _G	Gate to Source Charge Gate to Drain Charge Total Gate Charge $V_{DS} = 600 \text{ V}, _{D} = 100 , _{GS} = -4/+15 , _{Per} _{IEC60747-8-4}$	- og 21	68 80 222	-	nC

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(T,=25⁰C	UNLESS	OTHERWISE	SPECIFIED)
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SYMBOL	PARAMETER		MIN	ТҮР	MAX	UNIT		
V _{SD}	Diode Forward Voltage $I_{SD} = 70A, V_{GS} = -4V$	T」= 25°C T」= 150°C	-	4.6 4.1	-	V		
lF	Continuous Diode Forward Current	$T_J = 25^{\circ}C$	-	-	90	А		
I _{F,pulse}	Diode Pulse Current V_{GS} = -4 V, Pulse Width t_p limited by T_{Jmax}		-	-	450	А		
Qrr	Reverse Recovery Charge VGS = -4 V, ISD = 100 A, VR = 600 V di⊧/dt = 4600 A/µs, TJ = 175 °C		-	1700	-	nC		
trr	Reverse Recovery Time V _{GS} = -4 V, I _{SD} = 100 A, V _R = 600 V di _F /dt = 4600 A/µs, T _J = 175 °C		-	32	-	nS		
Irm	Peak Reverse Recovery Current V _{GS} = -4 V, I _{SD} = 100 A, V _R = 600 V di⊧/dt = 4600 A/µs, T _J = 175 °C		-	80	-	A		

Note: Production units are only tested at room temperature. Low/High temperature operation is guaranteed by design.

PACKAGE THERMAL AND MECHANICAL CHARACTERISTICS

SYMBO	L PARAMETER	MIN	TYP	MAX	UNIT
Ls	Stray Inductance (Per Leg)	-	12	-	nH
R _{0JB}	MOSFET Junction-to-Base Plate Thermal Resistance Per Leg	-	0.30	0.36	°C/W
Viso	Isolation to Base Plate	-	-	2500	VDC
TJ	Operating Junction Temperature	-55	-	175	°C
Tstg	Storage Temperature	-55	-	150	°C
	Mounting Torque for Module Mounting (see installation instructions) #4 Size Screw	3	-	4	in-lbs.
	Weight Module	-	15	20	g

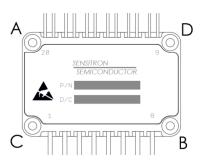
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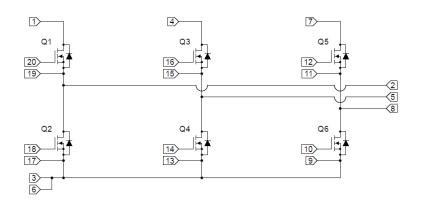
Installation instructions:

Recommended thermal interface material = Laird Tgon 805 (5 mil thick graphite pad)

- 1. Fasten screws to 1 to 2 in-lb of torque in the following sequence: A, B, C, D.
 - 2. Fasten screws to final torque in the same sequence: A, B, C, D

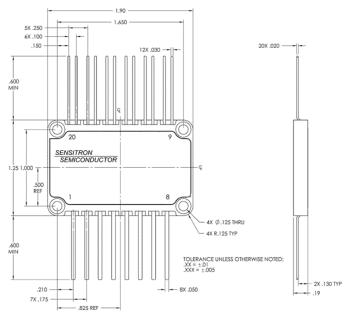


Schematic Diagram:



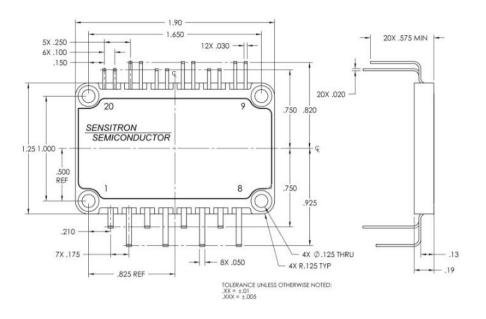
Mechanical Outline (inches):

Part Number SPM1018





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Part Number SPM1018L

Part Number Description

"SPM1018": Straight leads "SPM1018L": Leads bent up in staggered configuration

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