

TECHNICAL DATA DATA SHEET 704, REV. A

# N-Channel Enhancement Mode Vertical DMOS FET

- Free From Secondary Breakdown
- Low Power Drive Requirement
- Ease of Paralleling
- Low C<sub>ISS</sub> and Fast Switching Speeds
- Excellent Thermal Stability
- Integral Source-Drain Diode
- High Input Impedance and High Gain

#### **MAXIMUM RATINGS**

ALL RATINGS ARE AT  $T_A = 25^{\circ}\text{C}$  UNLESS OTHERWISE SPECIFIED.

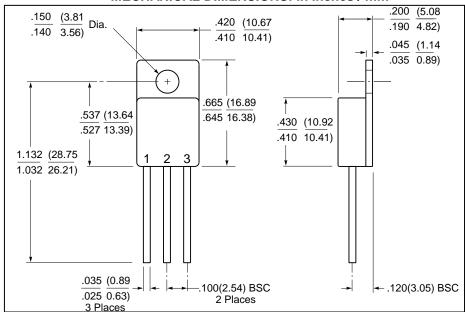
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	$V_{GS}$	-	-	±20	Volts
CONTINUOUS DRAIN CURRENT @ $T_C = 25^{\circ}$	C I <sub>D</sub>	-	-	100	mA
LIMITED BY MAXIMUM RATED T <sub>J</sub>					
PULSED DRAIN CURRENT @ $T_C = 25^{\circ}$	C I <sub>DM</sub>	-	-	300	mA (pk)
OPERATING AND STORAGE TEMPERATURE	$T_{OP}/T_{STG}$	-55	-	+150	°C
TERMAL RESISTANCE JUNCTION TO CASE	$R_{ hetaJC}$	-	-	23.5	°C/W
TOTAL DEVICE DISSIPATION @ T <sub>C</sub> = 25°C	P <sub>D</sub>	-	-	5.3	Watts

# **ELECTRICAL CHARACTERISTICS**

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DRAIN TO SOURCE BREAKDOWN VOL	_TAGE	$BV_{DSS}$	500	-	-	Volts
$V_{GS}$	$= 0V, I_D = 1.0mA$					
GATE THRESHOLD VOLTAGE V <sub>DS</sub> =		$V_{GS(th)}$	2.0	-	4.0	Volts
DRAIN TO SOURCE ON STATE RESIST						
$V_{GS} = 0$	$5Vdc$ , $I_D = 50mA$	$R_{DS(ON)}$	-	45		Ω
$V_{GS} = 10 \text{Vdc}, I_D = 50 \text{mA}$		-(- /		40	60	
ZERO GATE VOLTAGE DRAIN CURRENT			-	-		
$V_{DS} = Max. Rating, V_{GS} = 0Vdc$		$I_{DSS}$			10	μΑ
$V_{DS} = 0.8 \text{xMax}$ . Rating					1.0	mA
$V_{GS} = 0$	$Vdc, T_A = 125^{\circ}C$					
GATE TO BODY LEAKAGE CURRENT	$V_{GS} = \pm 20 Vdc$	I <sub>GSS</sub>	-	-	±100	nA
	$V_{DS} = 0$					
TURN ON DELAY TIME	$V_{DD} = 25V$ ,	t <sub>d(ON)</sub>	-	-	10	nsec
RISE TIME	$I_{D} = 150 \text{mA},$	`t <sub>r</sub> ´			15	
TURN OFF DELAY TIME	$R_G = 25\Omega$	$t_{d(OFF)}$			10	
FALL TIME	-	`t <sub>f</sub>			10	
FORWARD TRANSCONDUCTANCE		g <sub>fs</sub>	50	100	-	mS (1/Ω)
$V_{DS}$ :	$= 25V, I_D = 50mA$					,
REVERSE RECOVERY TIME	$I_{S} = 0.5A,$	t <sub>rr</sub>	-	300	-	nsec
REVERSE RECOVERY CHARGE	$V_{GS} = 0$					
INPUT CAPACITANCE	$V_{DS} = 25V dc$	C <sub>iss</sub>	-	45	55	pF
OUTPUT CAPACITANCE	$V_{GS} = 0V dc$	$C_{oss}$		8.0	10	
REVERSE TRANSFER CAPACITANCE	f = 1 MHz	$C_{rss}$		2.0	5.0	

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## **MECHANICAL DIMENSIONS: in Inches / mm**



TO-257

#### **PINOUT TABLE**

DEVICE TYPE	PIN 1	PIN 2	PIN 3
N CHANNEL MOSFET IN A	DRAIN	SOURCE	GATE
TO-257 PACKAGE			

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