TECHNICAL DATA DATA SHEET 778, REV. B

HERMETIC POWER MOSFET N-CHANNEL

FEATURES:

- 60 Volt, 0.020 Ohm, 55A MOSFET
- Isolated Hermetic Ceramic Package
- Fast Switching
- Low R_{DS (on)}
- Equivalent to IRFN054

MAXIMUM RATINGS

ALL RATINGS ARE AT $T_{\rm C}$ = 25°C UNLESS OTHERWISE SPECIFIED.

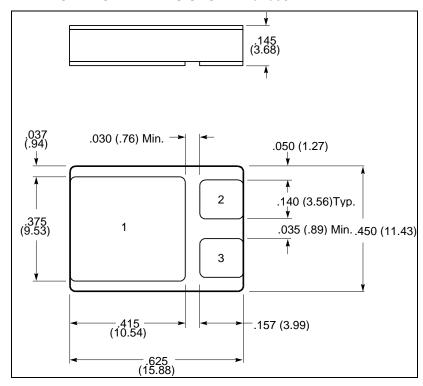
RATING		SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE		V_{GS}	•	-	±20	Volts
ON-STATE DRAIN CURRENT	@ T _C = 25°C	I _D	-	-	55	Amps
$V_{GS} = 10V$	@ $T_C = 100^{\circ}C$		-	-	40	
PULSED DRAIN CURRENT	@ $T_C = 25^{\circ}C$	I _{DM}	•	-	220	Amps
OPERATING AND STORAGE TEMPERATURE		T_{OP}/T_{STG}	-55	-	+150	°C
THERMAL RESISTANCE, JUNCTION TO CASE		$R_{\theta hJC}$			0.83	°C/W
TOTAL DEVICE DISSIPATION @ T _C = 25°C		P_D	-	-	150	Watts

ELECTRICAL CHARACTERISTICS

DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV _{DSS}	60	-	-	Volts
$V_{GS} = 0V, I_{D} = 1.0m$	A				
STATIC DRAIN TO SOURCE ON STATE RESISTANCE		-	-		
$V_{GS} = 10V, I_{D} = 40A$	A R _{DS(ON)}			0.02	Ω
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$, $I_D = 250 \mu$	A $V_{GS(th)}$	2.0	-	4.0	Volts
FORWARD TRANSCONDUCTANCE	g _{fs}	20	-	-	S(1/Ω)
$V_{DS} \ge 15V$, $I_{DS} = 40$	A				, ,
ZERO GATE VOLTAGE DRAIN CURRENT		-	-		
$V_{DS} = 48V, V_{GS} = 0V$	I _{DSS}			25	μΑ
$V_{DS} = 48V, V_{GS} = 0V, T_{J} = 125^{\circ}C$				250	
GATE TO SOURCE LEAKAGE FORWARD V _{GS} = 20	V I _{GSS}	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE $V_{GS} = -20$	V			-100	
TURN ON DELAY TIME $V_{DD} = 30V$	t _{d(ON)}	-	-	33	
RISE TIME $I_D = 55A$,	Ì,			180	nsec
TURN OFF DELAY TIME $R_G = 2.350$	$t_{d(OFF)}$			100	
FALL TIME $V_{GS} = 10V$	t _f			100	
DIODE FORWARD VOLTAGE $T_C = 25^{\circ}C$, $I_S = 50^{\circ}A$, V _{SD}	-	-	2.5	Volts
$V_{GS} = 0$	V				
REVERSE RECOVERY TIME $T_J = 25^{\circ}C$, t _{rr}	-	-	280	
$I_{\rm F} = 50$					nsec
$di/dt \le 100A/\mu sec, V_{DD} \le 50V$	'				
INPUT CAPACITANCE V _{GS} = 0 \	/ C _{iss}	-	4265	-	
OUTPUT CAPACITANCE V _{DS} = 25 \			1746		pF
REVERSE TRANSFER CAPACITANCE f = 1.0MH;			493		

DATASHEET 778, REV. B

MECHANICAL DIMENSIONS: in Inches / mm



SMD-1

PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3
N-CHANNEL MOSFET, IN A SURFACE MOUNT SMD-1 PACKAGE	DRAIN	SOURCE	GATE

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