

TECHNICAL DATA
PART NUMBER SCP-5282-3, REV C

High Pulse Power Mil-STD-1275 Transorb



Application:

- +28V DC systems
- Bi-Directional

Protection Level:

- MIL-STD-1275 Compliant; 100V Surge withstanding with 0.5-ohm source impedance
 - ✓ 100% tested
- Capable of handling 130-msec single pulse up to 135A
- 100% tested for 5 pulses of 110A, 50 msec within 1 sec
- Designed to meet: SAE J1113-11 under following conditions:
 - ✓ 174V, 2-ohm upto 150msec
 - ✓ 174V, 5-ohm upto 350msec
- Designed to meet: ISO16750-2 under following conditions:
 - ✓ 151V surge, 1 ohm upto 350msec
- Designed to meet ISO7637-2 under following conditions:
 - ✓ 200V test pulse 3B

Key Features:

- Allows the use of 55V high efficiency FET
- Increase system reliability through eliminating avalanche FET operation
- Clamping below 55V DC for both 100V and 250V pulse
- High Pulse Power Capability
- Non-Hermetic version

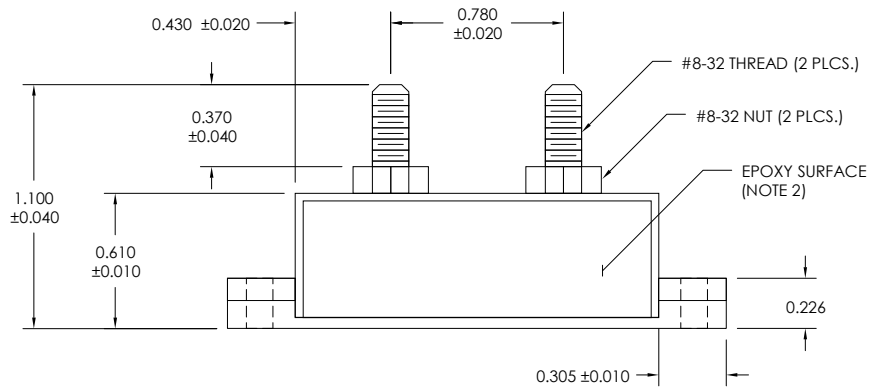
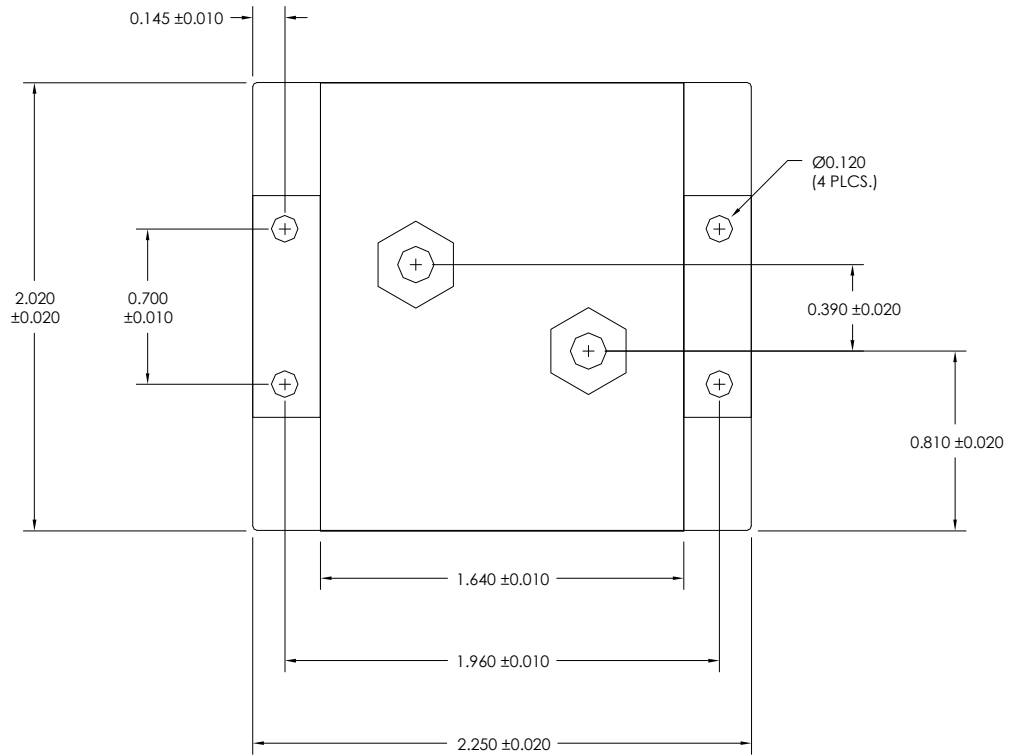
Part Ordering Information:

- SCP-5282-3: with threaded terminals

TECHNICAL DATA

Rating	Condition	Symbol	Min	Max	Units
Peak Pulse Power Dissipation	@ 25°C, 1ms	P_{pk}	-	100	KW
Steady State Power Dissipation	@ 25°C	P	-	60	Watts
Reverse Stand-Off Voltage	-	V_{WM}	-	33	Volts
Reverse Leakage	@ V_{WM}	I_D	-	40	μA
Breakdown Voltage	@ 10 mA	$V_{(BR)}$	36.7	-	Volts
Clamping Voltage	@ I_{PP}	V_c	-	49	Volts
Peak Pulse Current (single 130-msec square pulse)	-	I_{PP1}		135	Amps
Peak Pulse Current (5 pulses of 50-msec in 1 sec)	-	I_{PP2}		110	Amps
$T_{clamping}$	0 Volts to $V_{(BR)}$		-	$< 1 \times 10^{-8}$	Seconds
Operating & Storage Temp.	-	Top & Tstg	-55	+ 150	°C

TOP VIEW
MECHANICAL LAYOUT DRAWING



NOTES:

- 1) DIMENSIONS UNLESS OTHERWISE NOTED ARE IN INCHES.
- 2) POTTING SURFACE UNCONTROLLED.

SENSITRON

SEMICONDUCTOR

SCP-5282-3

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